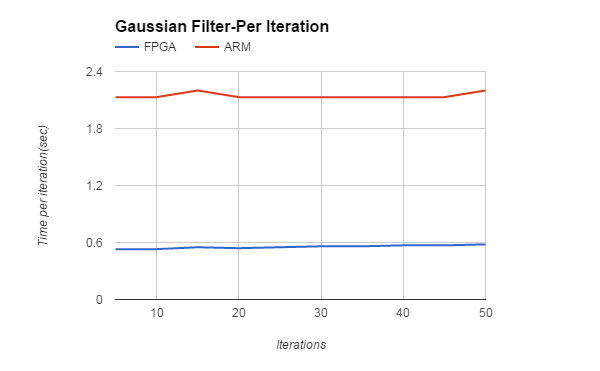
|  |  |  |
| --- | --- | --- |
| **Filter Iterations (Gaussian Filter)** | **FPGA** | **ARM** |
| 5 | 2.669 | 10.673 |
| 10 | 5.344 | 21.348 |
| 15 | 8.25 | 33.11 |
| 20 | 10.986 | 42.711 |
| 25 | 13.814 | 53.387 |
| 30 | 16.935 | 64.056 |
| 35 | 19.859 | 74.759 |
| 40 | 22.84 | 85.4 |
| 45 | 25.93 | 96.076 |
| 50 | 29 | 110 |



|  |  |  |
| --- | --- | --- |
| **Filter Iterations (Bilateral Filter)** | **FPGA (sec)** | **ARM (sec)** |
| 5 | 3.154 | 49.628 |
| 10 | 6.637 | 99.256 |
| 15 | 10.343 | 153.704 |
| 20 | 14.208 | 198.571 |
| 25 | 18.404 | 248.329 |
| 30 | 22.857 | 307.744 |
| 35 | 27.567 | 347.648 |
| 40 | 32.253 | 410.281 |
| 45 | 37.403 | 447.192 |
| 50 | 42 | 512 |

